

Notice of References CitedApplication/Control No.
09/586,191Applicant(s)/Patent Under
Reexamination
ISLES, ADRIAN J.Examiner
Ayal I SharonArt Unit
2123

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U.S. PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Name | Classification |
|---|---|--|-----------------|----------------------|----------------|
| | A | US-6,539,477 | 03-2003 | Seawright, J. Andrew | 713/100 |
| | B | US- | | | |
| | C | US- | | | |
| | D | US- | | | |
| | E | US- | | | |
| | F | US- | | | |
| | G | US- | | | |
| | H | US- | | | |
| | I | US- | | | |
| | J | US- | | | |
| | K | US- | | | |
| | L | US- | | | |
| | M | US- | | | |

FOREIGN PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Country | Name | Classification |
|---|---|--|-----------------|---------|------|----------------|
| | N | | | | | |
| | O | | | | | |
| | P | | | | | |
| | Q | | | | | |
| | R | | | | | |
| | S | | | | | |
| | T | | | | | |

NON-PATENT DOCUMENTS

| * | | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) |
|---|---|--|
| | U | Schneider, C. "A Parallel / Serial Trade-Off Methodology for Look-up Table Based Decoders" Proc. of the 34th Annual Conf. on Design Automation Conf. 1997. pp.498-503. |
| | V | Murgai, R. "Logic Synthesis for Programmable Gate Arrays" 27th Annual ACM/IEEE Design Automation Conference. 1991. pp.620-625. |
| | W | |
| | X | |

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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